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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
Patent Application

Inventor(s): Warren Snyder, Craig Nemecek and Bert Sullam

Application No.: 09/975,030

Group Art Unit:

Filed: 10/10/01

Examiner:

Title: EMULATOR CHIPBOARD ARCHITECTURE AND INTERFACE

Form 1449

**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<u>DMO</u>	<u>A</u>	<u>6,460,172</u>	<u>10/01/02</u>	<u>Insenser Farre et al.</u>	<u>716</u>	<u>17</u>	<u>06/21/00</u>

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	<u>B</u>							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	<u>C</u>	
Examiner <u>Darin Qui</u>	Date Considered <u>1-24-2006</u>	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.  
Include copy of this form with next communication to applicant.